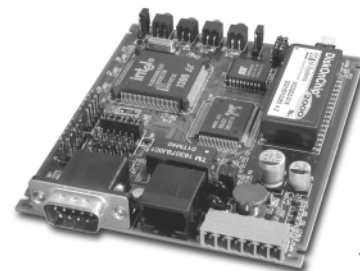


μFlashTCP-EP & μFlashPlus

- *Pre-installed DOS, TCP/IP and Web Server Software*
- *Flash File System*
- *10BASE-T Ethernet (NE2000 Compatible)*
- *386 Ex, 25 MHz / 512K Flash, 512K SRAM*
- *PC Compatible Serial Ports*
1 - RS232
1 - RS232 / RS485, Software Selectable
- *Ready to use, industry standard connectors*
- *32 Pin Dip Socket to accept 512K SRAM, 512K Flash, or M-Systems DiskOnChip products*
- *7V to 34V DC, 2 Watts nominal*
- *Rugged Enclosure with μFlashTCP-EP package*
- *10 Digital I/O lines (optional interface for enclosure)*



89-0040
μFlashTCP-EP



84-0040
μFlashPlus

Derived from the highly successful *μFlashTCP*, the *μFlashTCP-EP* and the *μFlashPlus* are ready-to-use platforms ideal for bridging existing equipment to Ethernet networks.

Providing a practical solution for designs requiring TCP/IP connectivity, the *μFlashTCP-EP* and the *μFlashPlus* pack a DOS based computer with Ethernet and PC compatible serial ports into a compact form factor. The powerful Intel 386Ex processor and DOS operating system allow design and debugging in a familiar environment, while industry standard connectors simplify direct connection to 10BASE-T (NE2000 compatible) networks.

Additional features include a watchdog timer, on board voltage regulator, 10 digital I/O lines, RS-485 serial port capability and status LEDs. Optional memory expansion of 512K SRAM, 512K Flash, M-Systems DiskOnChip or a battery backed clock calendar with 128K SRAM is available using the 32 Pin Dip socket.

The *μFlashPlus* is the board level version of the *μFlashTCP-EP* controller, but does not include the rugged enclosure package.

The *μFlashTCP-EP Development Kit* (99-0041) is available for development of either the *μFlashTCP-EP* or *μFlashPlus* and includes *μFlashTCP-EP* controller, Programming Cable, Ethernet Cable, 110 VAC Adapter, Setup Guide, Schematic, and CD with Borland C/C++ V4.52 Compiler, utilities, sample programs and documentation. (220Vin Development Kit 99-0042 available.) Also included within the development kits are sample programs and source code for the TCP/IP stack and web server that can be compiled with the Borland C/C++ package.

For I/O accessibility on the *μFlashTCP-EP* package, the I/O BreakOut upgrade provides 10 I/O lines with ground via wire terminals through the front faceplate. The I/O BreakOut option can be purchased as a peripheral kit (99-0048) or as a complete *μFlashTCP-EP I/O BreakOut* assembly (89-0047).



89-0047
μFlashTCP-EP
I/O BreakOut

JK microsystems, Inc.

1403 5th St. Suite D, Davis, CA 95616
<http://www.jkmicro.com>

Phone (530)297-6073
Fax (530)297-6074

μFlashTCP-EP & μFlashPLUS

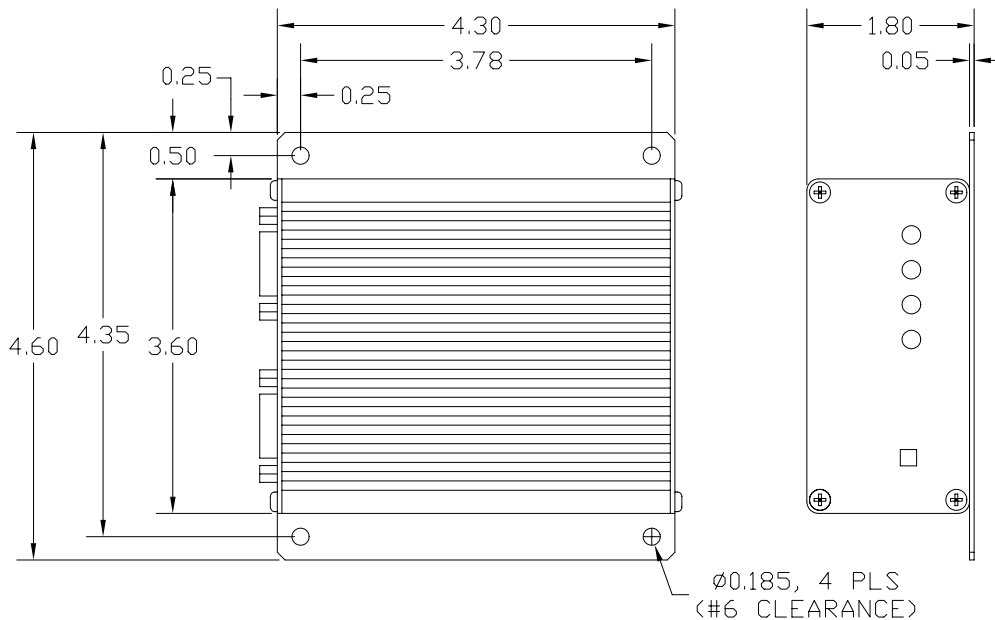
Specifications

Processor	25Mhz Intel 386Ex
Operating System	XDOS (MS/PC DOS 3.3 compatible)
Memory	512K SRAM, 512K Flash
Ethernet	10BASE-T, NE2000 compatible Link status and Activity LEDs
Serial Port 1	RS-232 with 5 handshake lines COM1, address 0x3F8, IRQ 4 115200 baud maximum
Serial Port 2	RS-232 no handshaking or RS-485 half duplex, COM2, address 0x2F8, IRQ 3 115200 baud maximum
Watchdog	Programmable timeout, Generates processor NMI
Sync. Serial	Full duplex, Independent Rx and Tx clocks, Master or Slave operating mode
Supply Voltage	7-34V DC ± 10%
Power Consumption	2 Watts (nominal)
Humidity	5 - 90%, non-condensing

Temperature	-20 to 85°C
Weight	12 oz (340 gm)
Dimensions without baseplate	3.6" x 4.3" x 1.8" (91.4mm x 109.2mm x 44.5mm)
with baseplate	4.6" x 4.3" x 1.8" (116.8mm x 109.2mm x 44.5mm)
Digital I/O	10 Bits (P3.0-P3.5 & P1.4-P1.7) Pin configurable as input or output P3.3 and P3.4 configurable as interrupts 8mA source/sink

Optional Features

- Additional M-Systems DiskOnChip, 512K SRAM, 512K Flash or Battery backed clock calendar with 128K SRAM (20-0074).
- Digital I/O interface option using I/O BreakOut Kit (99-0048) or purchasing preinstalled I/O BreakOut package (89-0047).



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